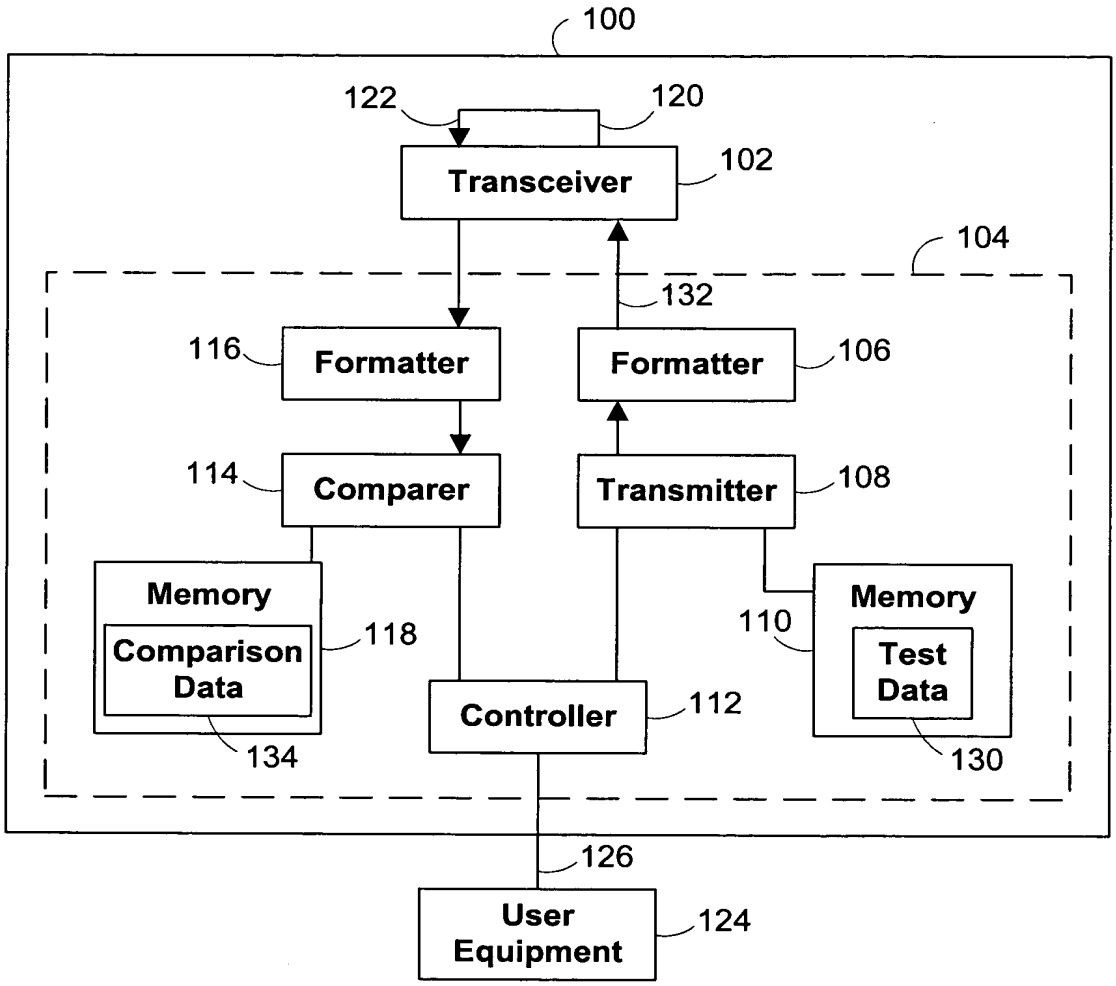
100
122
120
Transceiver
102
104
132
116
Formatter
Formatter
106
114
Comparer
Transmitter
108
Memory
Comparison Data
118
134
Controller
112
110
Memory
Test Data
130
126
User Equipment
124

FIG. 1

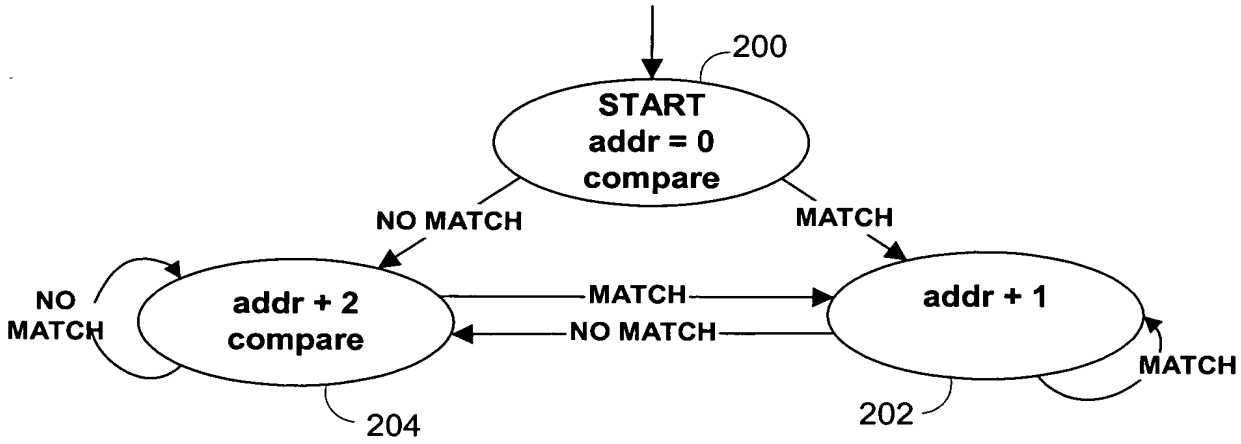
200
START
addr = 0
compare
NO MATCH
MATCH
NO
MATCH
addr + 2
compare
MATCH
NO MATCH
addr + 1
MATCH
204
202

FIG. 2

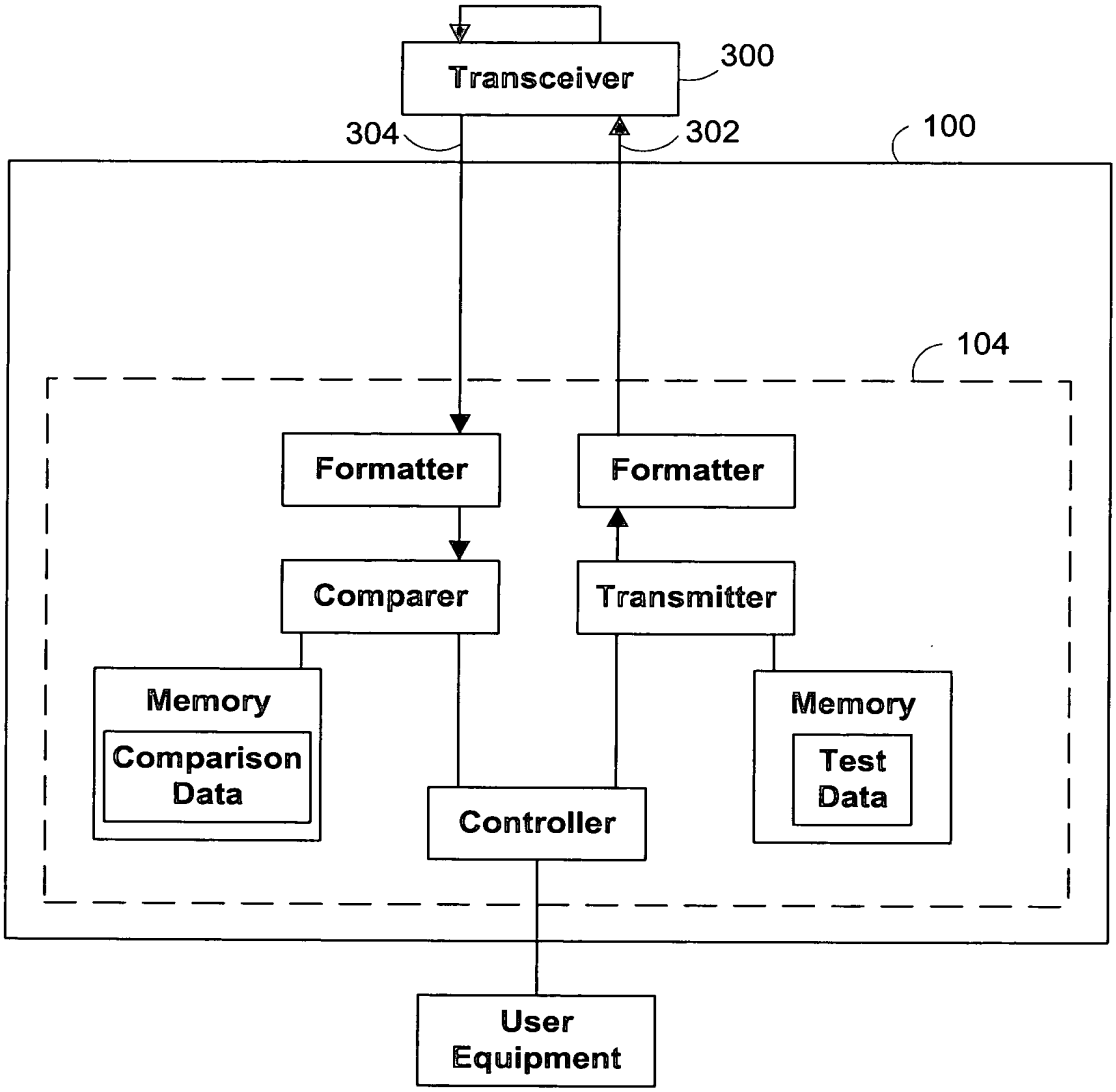
Transceiver
300
304
302
100
104
Formatter
Formatter
Comparer
Transmitter
Memory
Comparison Data
Memory
Test Data
Controller
User Equipment

FIG. 3

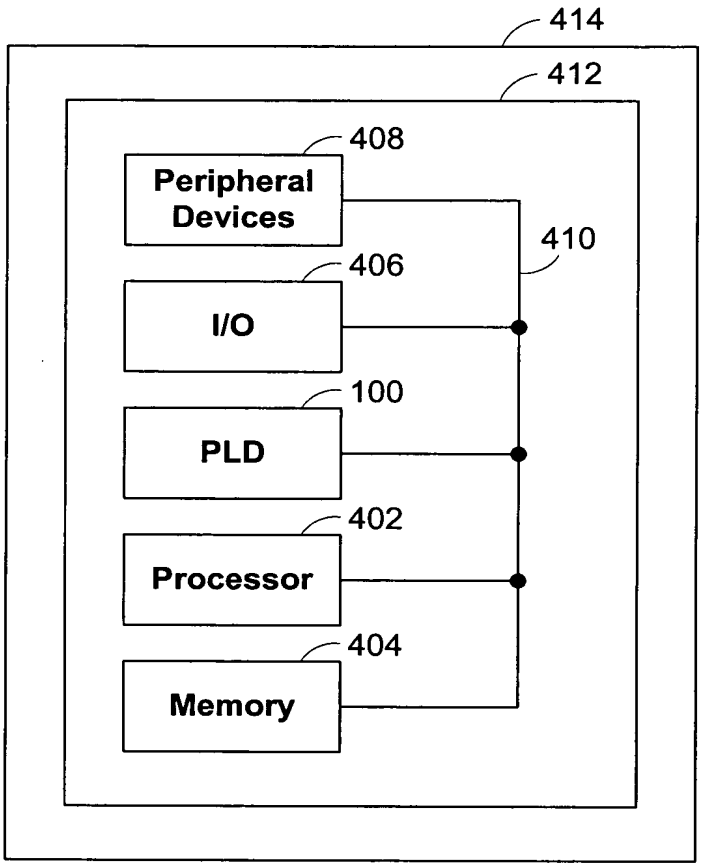
414
412
408
Peripheral Devices
410
406
I/O
100
PLD
402
Processor
404
Memory

FIG. 4